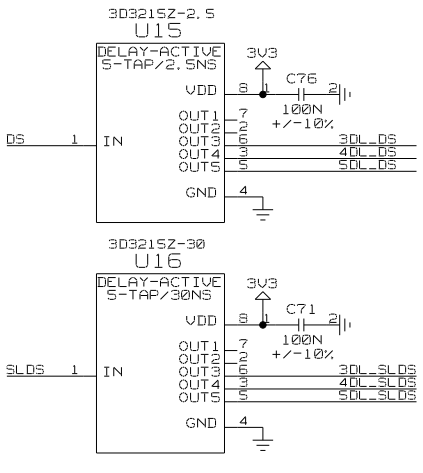
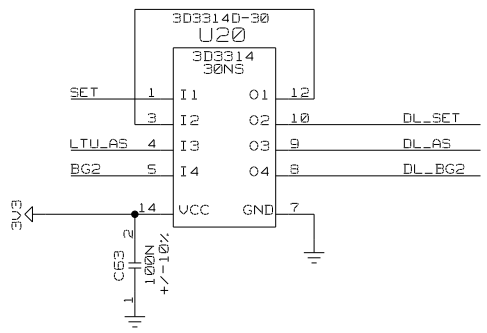
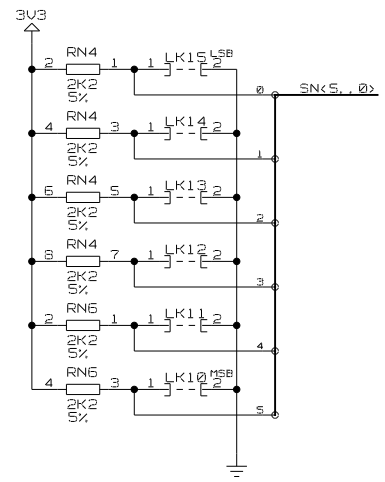
DELAY LINES

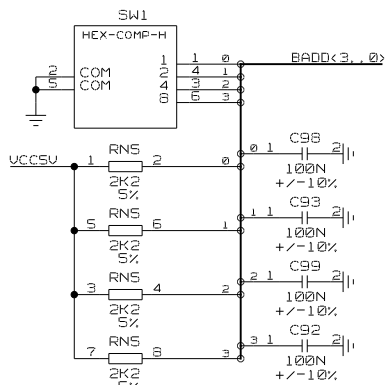




SERIAL NUMBER

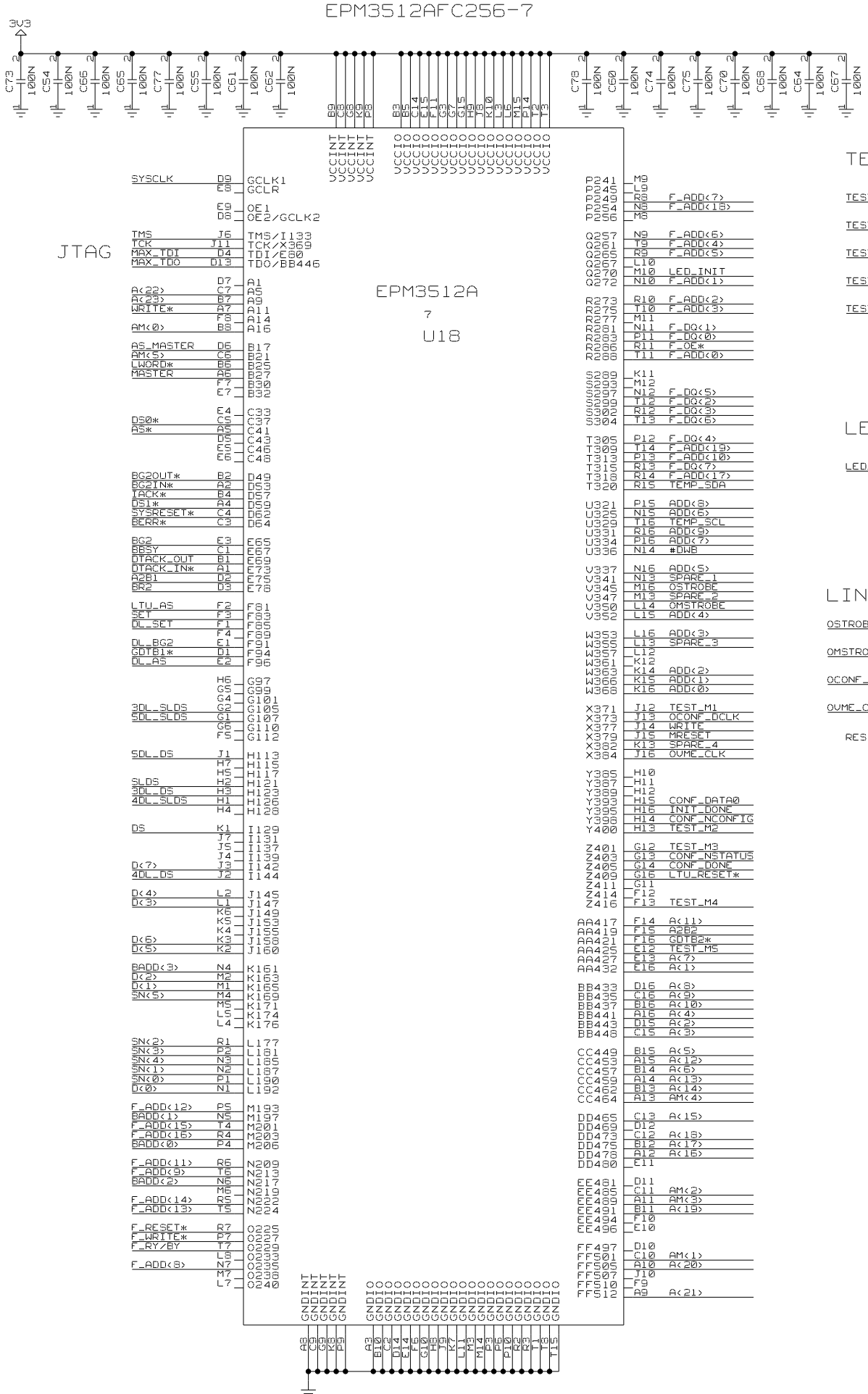


BOARD ADDRESS

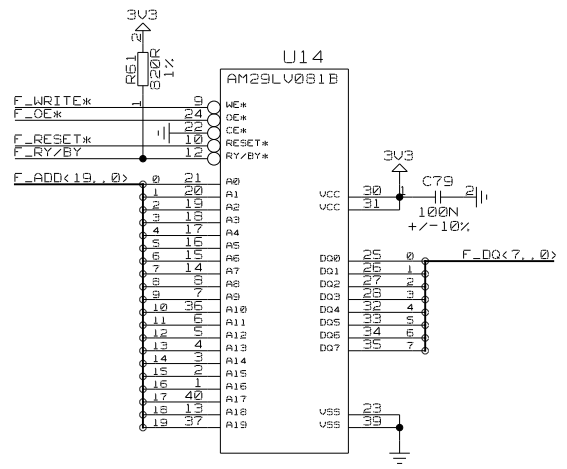


VME INTERFACE CONTROLLER

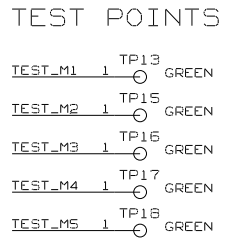
EPM3512AFC256-7



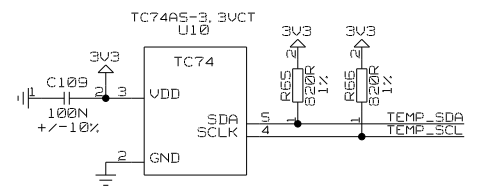
FLASH MEMORY



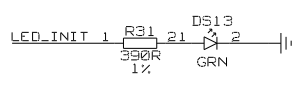
TEST POINTS



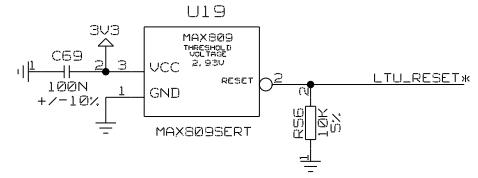
TEMPERATURE SENSOR



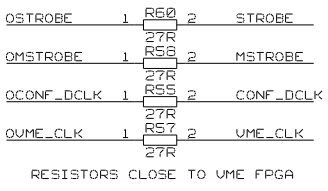
LED FPGA INIT.



POWER-ON RESET



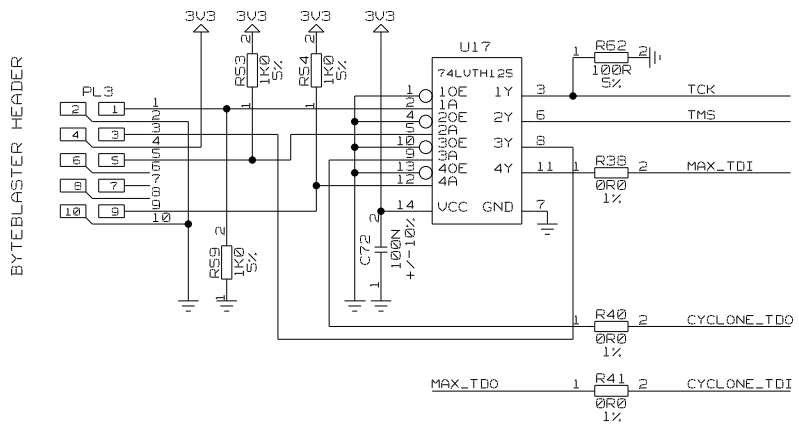
LINE TERMINATION



RESISTORS CLOSE TO VME FPGA

50 OHM TRACKS

JTAG CONNECTION



ISSUE	DATE	MOD NO.	DRN BY.	CHKD.	APPD.	STATUS
B	25-05-2004	TE245	P. JOVANOVIC	P. JOVANOVIC	P. JOVANOVIC	
A	15-12-2003	- - - - -	P. JOVANOVIC	P. JOVANOVIC	P. JOVANOVIC	

USED ON: ALICE CENTRAL TRIGGER PROCESSOR

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CCLRC COUNCIL FOR THE CENTRAL LABORATORY OF THE RESEARCH COUNCILS
RUTHERFORD APPLETON LABORATORY, CHILTON, OXON OX11 0QX.

TITLE

PC3265M CIRCUIT

LOCAL TRIGGER UNIT (LTU)

VME INTERFACE CONTROLLER

A - 45/03
B - 37/04

A | 2-TE-0119-001-03-B

TOTAL NO. OF SHEETS 5