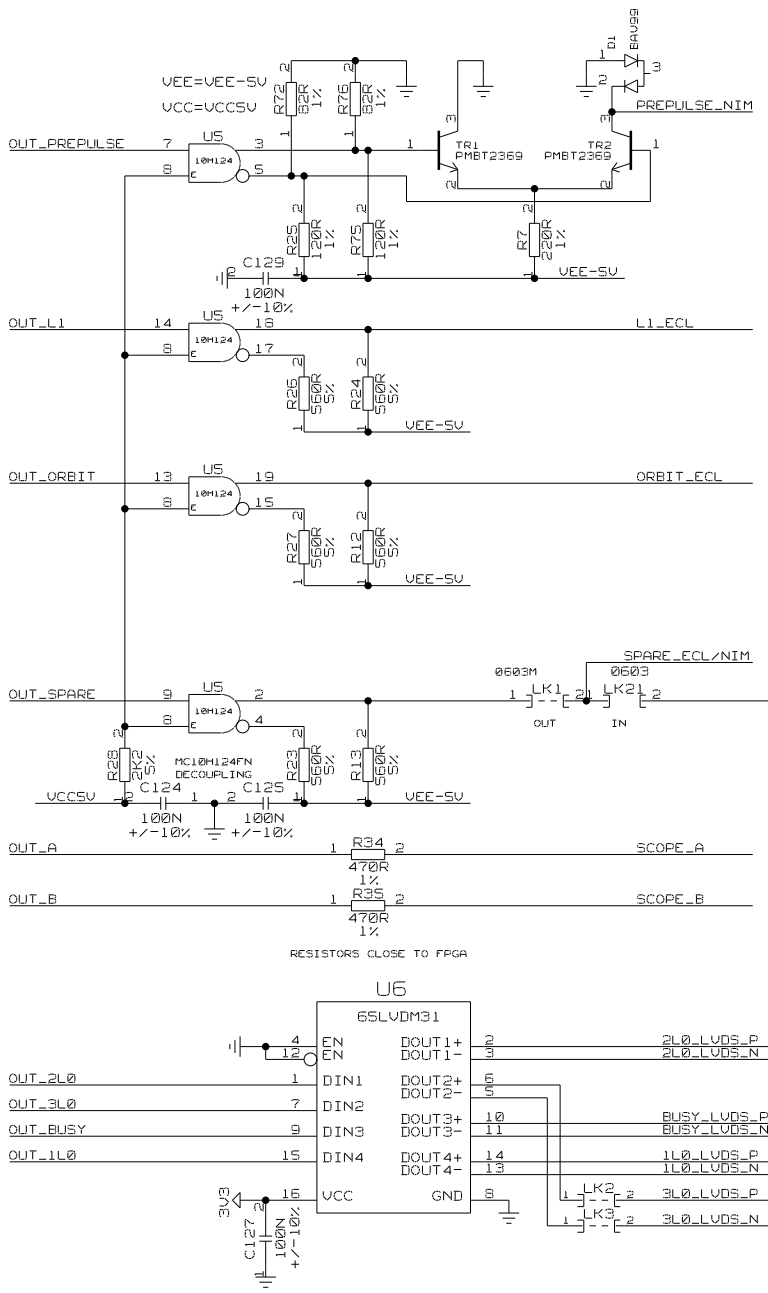
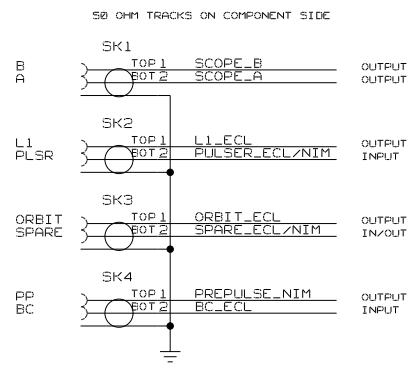
A | 2-TE-0119-001-02-B | THIS DRAWING CONFORMS TO B.S. 3939

FRONT PANEL CONNECTIONS

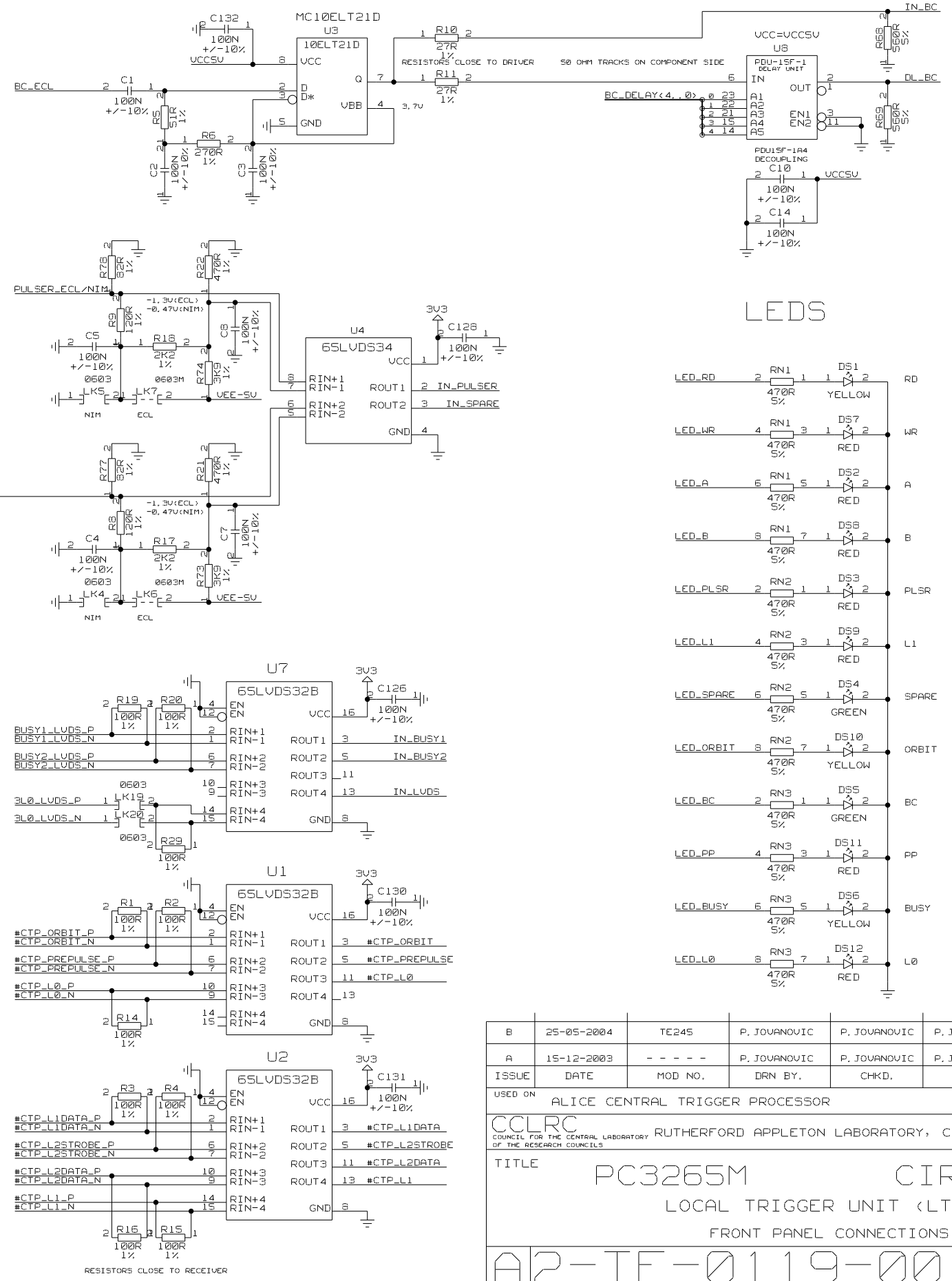
LTU OUTPUTS



CONNECTORS



LTU INPUTS



BC DELAY

LEDS

ALL THE INTERNAL SIGNALS CONNECTED TO THE LTU FPGA <SHEET 4>

ISSUE	DATE	MOD NO.	DRN BY.	CHKD.	APPD.	STATUS
B	25-05-2004	TE245	P. JOVANOVIC	P. JOVANOVIC	P. JOVANOVIC	
A	15-12-2003	- - - - -	P. JOVANOVIC	P. JOVANOVIC	P. JOVANOVIC	

USED ON: ALICE CENTRAL TRIGGER PROCESSOR

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CCLRC COUNCIL FOR THE CENTRAL LABORATORY OF THE RESEARCH COUNCILS
RUTHERFORD APPLETON LABORATORY, CHILTON, OXON OX11 0QX.

TITLE

PC3265M CIRCUIT

LOCAL TRIGGER UNIT (LTU)

FRONT PANEL CONNECTIONS

A - 45/03
B - 37/04

A | 2-TE-0119-001-02-B

TOTAL NO. OF SHEETS 5

A 2-TE-0119-001-02-B