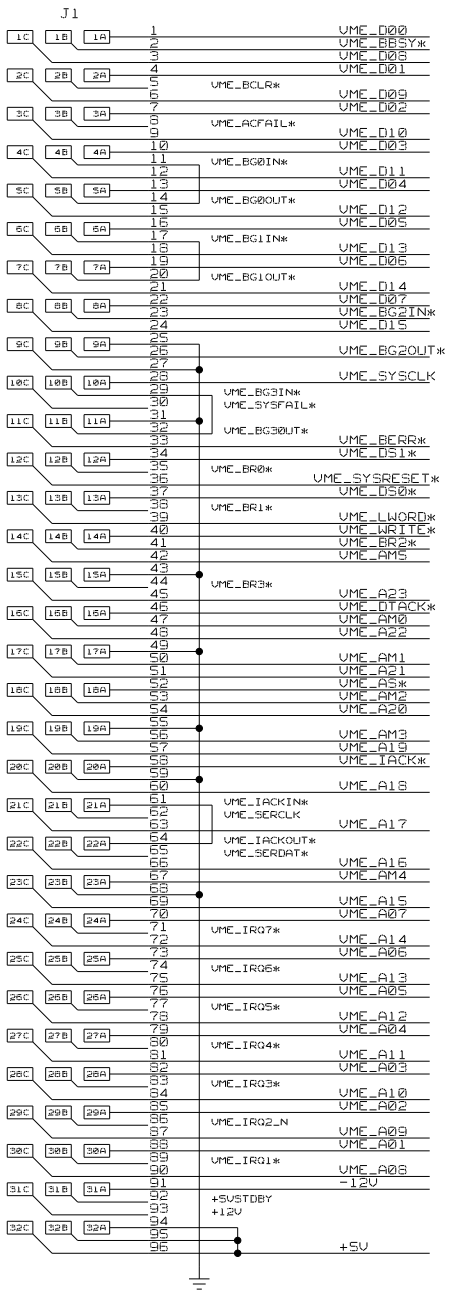
A | 2-TE-0119-001-01-B | THIS DRAWING CONFORMS TO B.S. 3939

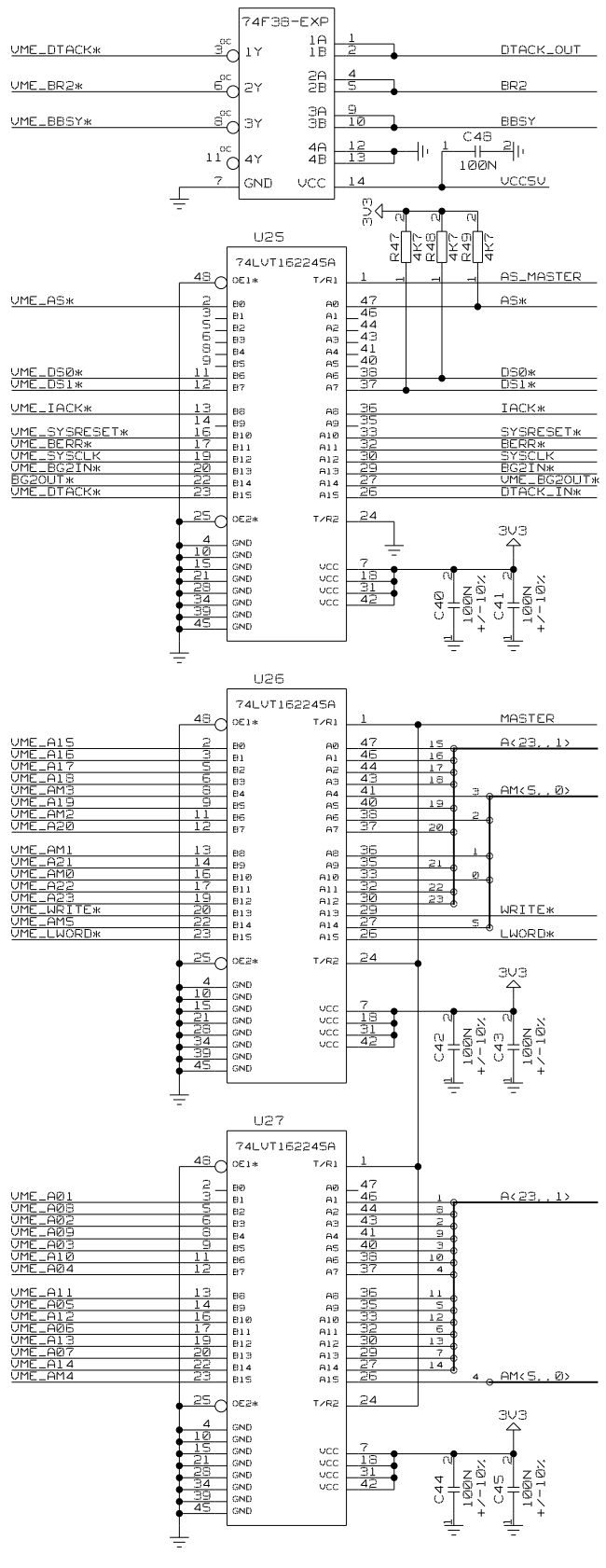
VME CONNECTIONS

VME J1 CONNECTIONS

PL-DIN41612-96

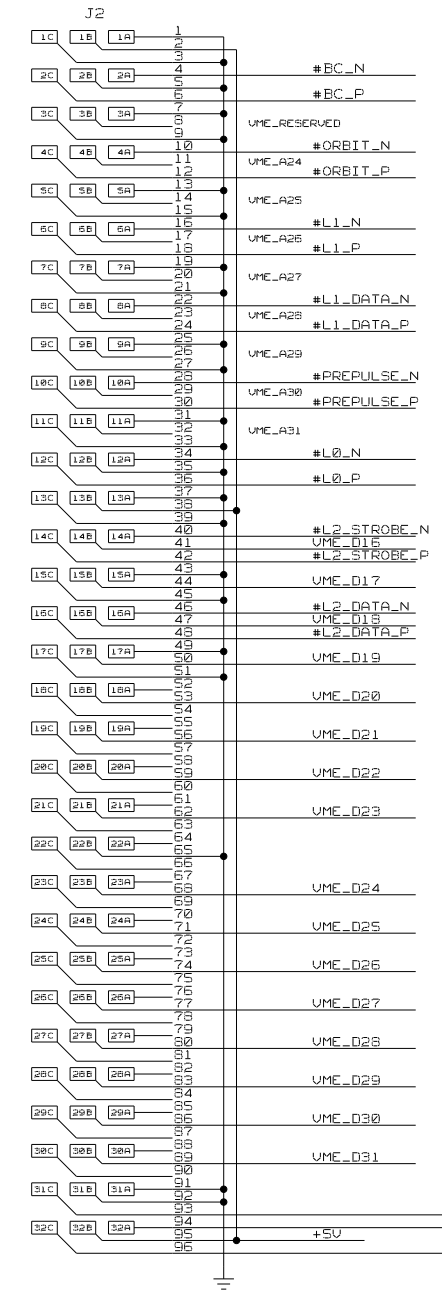


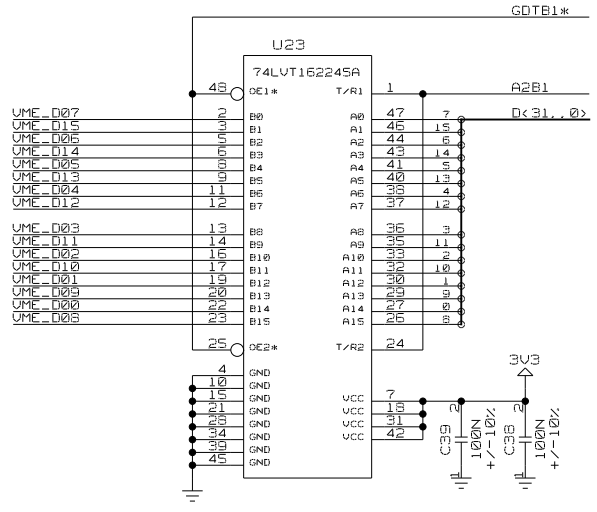
VME_BGXIN/OUT* AND VME_BR2*
TRACKS ON SOLDER SIDE

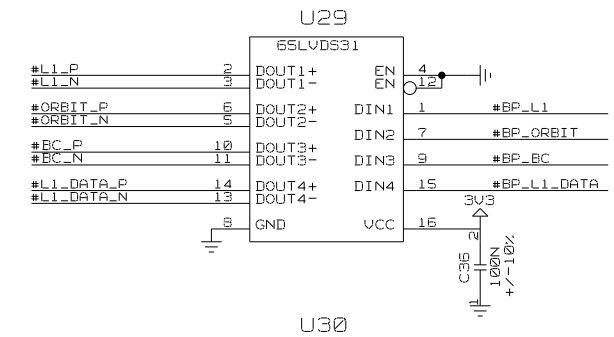


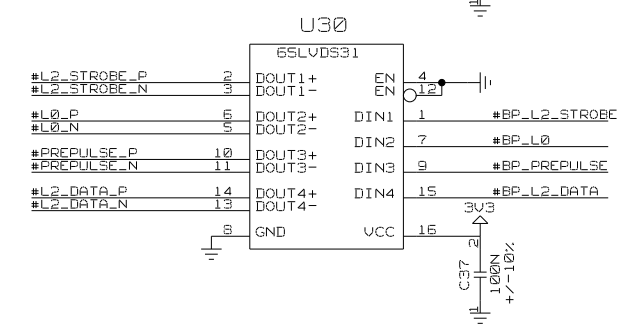
VME J2 CONNECTIONS

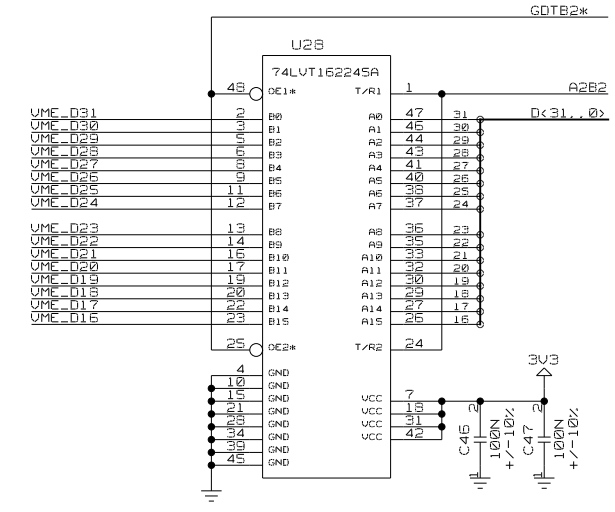
PL-DIN41612-96











I2C CONTROL BUS <SHEET 5>
SCL
GND/VDD
SDA

LTU FPGA <SHEET 4>

ISSUE	DATE	MOD NO.	DRN BY.	CHKD.	APPD.	STATUS
B	25-05-2004	TE245	P. JOVANOVIC	P. JOVANOVIC	P. JOVANOVIC	
A	15-12-2003	- - - - -	P. JOVANOVIC	P. JOVANOVIC	P. JOVANOVIC	

USED ON: ALICE CENTRAL TRIGGER PROCESSOR

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COUNCIL FOR THE CENTRAL LABORATORY OF THE RESEARCH COUNCILS
RUTHERFORD APPLETON LABORATORY, CHILTON, OXON OX11 0QX.

TITLE

PC3265M CIRCUIT

LOCAL TRIGGER UNIT (LTU)

VME CONNECTIONS

A - 45/03
B - 37/04

A | 2-TE-0119-001-01-B

TOTAL NO. OF SHEETS 5

A | 2-TE-0119-001-01-B